



UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED
UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/807,522	04/12/2001	Hans-Detlef Groeger	P01.0132	9081
26574	7590	08/24/2004	EXAMINER	
SCHIFF HARDIN, LLP PATENT DEPARTMENT 6600 SEARS TOWER CHICAGO, IL 60606-6473				PHAN, RAYMOND NGAN
ART UNIT		PAPER NUMBER		
		2111		

DATE MAILED: 08/24/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/807,522	GROEGER ET AL.
	Examiner Raymond Phan	Art Unit 2111

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 14 May 2004.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-14 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) 1-4 and 14 is/are allowed.
 6) Claim(s) 5 and 7-11 is/are rejected.
 7) Claim(s) 6,12 and 13 is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date _____.

4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.

5) Notice of Informal Patent Application (PTO-152)

6) Other: _____.

Part III DETAILED ACTION

Notice to Applicant(s)

1. This action is responsive to the following communications:
amendment filed on May 14, 2004
2. This application has been examined. Claims 1-14 are pending.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 5, 7-11 are rejected under 35 U.S.C. § 102(b) as being anticipated by Sexton et al. (US No. 5,072,374).

In regard to claim 5, Sexton et al. disclose the method comprising a parallel bus (i.e. common bus); a plurality of module connected to the parallel bus wherein the plurality of module each including a processor, the memory device and a DMA controller (see figure 2), the bus interface connecting the plurality of module in the parallel databus such that the data are transmitted between the transmitter (i.e. master) of the plurality of slaves with messages without the use of processor (see col. 3, line 53 through col. 4, line 62). But Sexton et al. do not specifically disclose the bus controller of the transmitter being fashioned such that the transmitter programs the DMA controller to read out data stored in the memory of the transmitter and

send them to the receiver in response to the request message of the receiver. However Kato et al. disclose the system such that the first CPU 1a programming the first DMA 2a to transmit the data to the unit 10b see figure 2, col. 5, line 8 through col. 6, line 35). Therefore, it would have been obvious to a person of an ordinary skill in the art at the time the invention was made to have combined the teachings of Kato et al. within the system of Sexton et al. because it would provide a faster data transferring and relieve the burden on the processors.

In regard to claim 7, Sexton et al. disclose the step of sending plurality of data messages respectively containing data packet to the slave from the bus interface of the master following the reception of the request message (see col. 3, line 53 through col. 4, line 62).

In regard to claim 8, Sexton et al. disclose the step of sending information for programming the DMA controller (see col. 3, line 53 through col. 4, line 62).

In regard to claim 9, Sexton et al. disclose the step of programming the DMA controller to transmit the stored data in the memory (see col. 3, line 53 through col. 4, line 62).

In regard to claim 10, Sexton et al. disclose the programming the DMA controller for reception of the data with transmission of the request message (see col. 3, line 53 through col. 4, line 62).

In regard to claim 11, Sexton et al. disclose the transmitting of data with plurality of message that respectively contained a data packet (see col. 3, line 53 through col. 4, line 62).

Allowable Subject Matter

5. Claims 1-4, 14 are allowable over the prior art of records.
6. Claims 6, 12-13 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
7. The following is an Examiner's statement of reasons for the indication of allowable subject matter: Claims 1, 6, 14 is allowable over the prior art of record because the Examiner found neither prior art cited in its entirety, nor based on the prior art, found any motivation to combine any of the said prior arts which the bus controller of the transmitter assembly being fashioned such that the transmitter assembly programs the DMA controller to read out data stored in the memory device of the transmitter assembly without making use of the processor of the transmitter assembly.

The remaining claim, not specifically mentioned, is allowed for the rationale as set forth claims 1, 6.

Response to Arguments

8. In view of remarks filed on May 14, 2004, claims 5, 7-11 have been fully considered but they are not deemed to be persuasive.

In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., the bus controller of the transmitter assembly being fashioned such that the transmitter assembly programs the DMA controller to read out data stored in the memory device of the transmitter

assembly without making use of the processor of the transmitter assembly) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the

Conclusion

9. Claims 5, 7-11 are rejected. Claims 1-4, 14 are allowed. Claims 6, 12-13 are objected.

10. The prior arts made of record and not relied upon are considered pertinent to applicant's disclosure.

Mergard et al. (US No. 5,941,968) disclose a computer system for concurrent data transferring between graphic controller and unified system memory and between CPU and expansion device.

Bridges et al. (US No. 6,055,584) disclose a processor local bus posted DMA flyby burst transfers.

Duncan et al. (US No. 6,012,120) disclose a method and apparatus for providing DMA transfers between devices coupled to different host bus bridges.

11. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 C.F.R. § 1.136(a).

A SHORTENED STATUTORY PERIOD FOR RESPONSE TO THIS FINAL ACTION IS SET TO EXPIRE THREE MONTHS FROM THE DATE OF THIS ACTION. IN THE EVENT A FIRST RESPONSE IS FILED WITHIN TWO MONTHS OF THE MAILING DATE OF THIS FINAL ACTION AND THE ADVISORY ACTION IS NOT MAILED UNTIL AFTER THE END OF THE THREE-MONTH SHORTENED STATUTORY PERIOD, THEN THE SHORTENED STATUTORY PERIOD WILL EXPIRE ON THE DATE THE ADVISORY ACTION IS MAILED, AND ANY EXTENSION FEE PURSUANT TO 37 C.F.R. § 1.136(a) WILL BE CALCULATED FROM THE MAILING DATE OF THE ADVISORY ACTION. IN NO EVENT WILL THE STATUTORY PERIOD FOR RESPONSE EXPIRE LATER THAN SIX MONTHS FROM THE DATE OF THIS FINAL ACTION.

12. Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Raymond Phan, whose telephone number is (703) 306-2756. The examiner can normally be reached on Monday-Friday from 6:30AM- 4:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's Primary, Paul Myers can be reached on (703) 305-9656 or via e-mail addressed to paul.myers@uspto.gov. The fax phone number for this Group is (703) 872-9306.

Communications via Internet e-mail regarding this application, other than those under 35 U.S.C. 132 or which otherwise require a signature, may be used by the applicant and should be addressed to [raymond.phan@uspto.gov].

All Internet e-mail communications will be made of record in the application file. PTO employees do not engage in Internet communications where there exists a possibility that sensitive information could be identified or exchanged unless the record includes a properly signed express waiver of the confidentiality requirements of 35 U.S.C. 122. This is more clearly set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and Trademark on February 25, 1997 at 1195 OG 89.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 305-3900.



PAUL R. MYERS
PRIMARY EXAMINER

RP
Raymond Phan
8/23/04